

## AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph number 24, beginning at page 6, with the following rewritten paragraph:

In accordance with certain embodiments of the present invention, expansion card 102 comprises a mating detection circuit 120. Mating detection circuit 120 may determine whether any combination of cable connector(s) and/or card connector(s) is mated with their counterparts in computer system 100. In Figure 1, for example, mating detection circuit 120 may determine whether cable connectors 108A/108B, cable connectors 110A/110B and/or [[AGP]] AGP card connectors 106A/106B are mated; that is, whether the expansion card is installed in computer system 100. In one exemplary embodiment, mating detection circuit 120 detects whether the cable connectors; that is, power connectors 110A/110B and USB connectors 108A/108B, are mated. To make such a determination, mating detection circuit 120 monitors, in one particular implementation, the 5VDC signal 126 received through power connector 110A and the USB2 power signal 136 received through USB connector 108A.

Please replace the paragraph number 26, beginning at page 6, with the following rewritten paragraph:

Figure 2 is a schematic diagram of a standard AGP connector 106A utilized in certain embodiments of the present invention. Of the 132 contacts provided in standard AGP connector 106A, contacts A4 and B4 carry a USB data signal. In the illustrative embodiment, expansion card 102 receives USB3 data+ signal 202A and USB3 data— signal 202B at contacts A4 and B4, respectively. It should be appreciated by those of ordinary skill in the art that, USB3 data+ signal 202A and USB3 data— signal 202B together comprise USB3 data signal 128 introduced above in connection with Figure 1. As shown in Figures 1 and 2, USB3 data signal 128 is routed to signal conditioning circuit 118C in the particular embodiment of the expansion card 102 illustrated in Figure 1. Contacts B5, B13, B31, B37, B49, B55, B61, A5, A13, A31, A37, A49, A55 and A61 of [[AGP]] AGP connector 106A are grounded in expansion card 102, as shown. All remaining contacts of AGP connector 106A are not used in this particular implementation.

Please replace the paragraph number 39, beginning at page 9, with the following rewritten paragraph:

Voltage doubling circuit 122 includes a feedback circuit 516 connecting the cathode of diode 508 to a feedback input (pin 5) of switching regulator 502. The voltage presented at the feedback input (pin 5) is determined by a voltage divider circuit comprising resistors 520A and 520B. Switching regulator 502 utilizes such feedback to determine the period of the FET drive signal generated at switch emitter (pin 2) to ensure insure the voltage at the anode of diode 508 is 24.7 VDC and, therefore, the output of voltage doubling circuit 122 is held at 24VDC.

Please replace the paragraph number 44, beginning at page 10, with the following rewritten paragraph:

USB1 data+ signal 302A and USB1 data— signal 302B are provided to contacts 2 and 3, respectively, of USB receptacle 602, as noted above. An optional electromagnetic interference (EMI) suppression circuit 616 may be included in signal conditioning circuit 118A to filter electromagnetic interference signals that may be carried on the signal lines that also carry USB1 data signals 302. In addition capacitors 610 and 612 may be included to adjust the rise and fall times of the USB1 data signals 302 as necessary to ensure insure signal integrity at USB receptacle 602.

Please replace the paragraph number 47, beginning at page 11, with the following rewritten paragraph:

A capacitor 612 may be connected between the conductor carrying +12VDC power signal 124 and ground. Capacitor 612 provides bulk decoupling of devices connected to USB-Plus-Power port 104A to ensure insure power is continually provided to such a connected device under conditions of a heavy transient power draw. A second, smaller capacitor 614 may also be connected between the conductor carrying +12VDC power signal 124 and ground to provide signal filtering of +12VDC power signal 124 prior to the power signal being presented at contacts 6 and 7 of power receptacle 604.